

## Microprocessor Engineering Limited

c Published by Microprocessor  
Engineering Copyright © 2003, 2005,  
2007, 2009, 2013 Microprocessor  
Engineering Limited  
MSP430 Lite Target v7.3  
User manual Manual,  
revision 7.3 22 June 2014  
Software Software version 7.3

For technical support Please contact  
your supplier

For further information  
MicroProcessor Engineering Limited  
133 Hill Lane Southampton SO15 5AF UK  
Tel +44 (0)23 8063 1441  
Fax +44 (0)23 8033 9691  
e-mail mpe@mpeforth.com  
e-mail tech-support@mpeforth.com  
web www.mpeforth.com

How to read the Glossary

Forth words are printed in upper case  
followed by a picture of the data  
stack, seperated by a backslash, the  
forth comment sign. This is done in  
the corosscompiler source to  
seperate the definition from a  
comment on it. Since the glossary is  
mostly generated automatic, this  
commenting stype is kept in this  
printing. There may be complex stack  
pictures. Only those forth words are  
given here, that are output by WORDS  
to Terminal.

## 4.2 Literal and flow of control

EXECUTE \ xt --  
I \ --n  
J \ --n  
UNLOOP \ --  
LEAVE \ --

## 4.3 Flash operations

FLERASE \ addr len --

C!F \ b addr --  
!F \ w addr --

## 4.4 Digits and strings

DIGIT \ char base -- 0 | n true  
/SRTING \ addr len n -- addr+n len-n  
CMOVE \ source dest len --  
CMOVE> \ source dest len --  
FILL \ addr len char --  
ERASE \ addr len --  
S= \ addr1 addr2 count --flag  
SKIP \ c-addr u char -- 'c-addr 'u  
SCAN \ caddr u char -- caddr2 u2  
COUNT \ addr --addr+1 len  
(") \ --addr  
UPC \ char -- char0  
UPPER \ c-addr len --  
PLACE \ c-addr1 u c-addr2 --

## 4.5 Arithmetic

### 4.5.1 Basics

1+ \ n -- n+1  
2+ \ n -- n+2  
1- \ n -- n-1  
2- \ n -- n-2  
2\* \ n1 -- n2  
U2/ \ n1 -- n2  
2/ \ n1 -- n2  
- \ n1 n2 -- n1-n2  
+ \ n1 n2 -- n1+n2  
NEGATE \ n1 ---n1  
ABS \ n1 --|n1|  
DNEGATE \ d1 ---d1  
DABS \ d1 --|d1|  
D+ \ d1 d2 -- d3  
D- \ d1 d2 -- d1-d2  
S>D \ n -- d  
D< \ d1 d2 -- t/f  
D> \ d1 d2-- t/f  
D0= \ d -- t/f  
D= \ d1 d2 -- t/f

### 4.5.2 Multiplication

UM\* \ u1 u2 -- ud  
\* \ n1 n2 -- n1\*n2

M\* \ n1 n2 -- d

### 4.5.3 Division

UM/MOD \ u32 u16 --urem uquot  
SM/REM \ d n --rem quot  
/MOD \ n1 n2 --rem quot  
/ \ n1 n2 -- quot  
MOD \ n1 n2 -- rem  
MU/MOD \ ud1 u2 --u3 ud4

## 4.6 Logic

AND \ n1 n2 -- n3  
OR \ n1 n2 -- n3  
XOR \ n1 n2 -- n3  
INVERT \ n1 --n2

## 4.7 Shifts

LSHIFT \ x count --x0  
RSHIFT \ x count --x0

## 4.8 Return stack words

>R \ x -- ; R -- x  
R@ \ --x ; R x -- x  
R> \ --x ; R:x--

## 4.9 Comparisons

= \ n1 n2 -- flag  
<> \ n1 n2 -- flag  
0<> \ n -- flag  
0= \ n -- flag  
0< \ n -- flag  
0> \ n -- flag  
U< \ n1 n2 -- flag  
U> \ n1 n2 -- flag

< \ n1 n2 -- t/f  
> \ n1 n2 -- t/f  
<= \ n1 n2 -- t/f  
>= \ n1 n2 -- t/f

MIN \ n1 n2 -- min(n1,n2)  
MAX \ n1 n2 -- max(n1,n2)

## 4.10 Stack primitives

OVER \ n1 n2 -- n1 n2 n1  
2OVER \ n1 n2 n3 n4 -- n1 n2 n3 n4 n1  
n2  
DROP \ n1 --

2DROP \ n1 n2 --  
SWAP \ n1 n2 - n2 n1  
2SWAP \ n1 n2 n3 n4 -- n3 n4 n1 n2  
DUP \ n1 -- n1 n1  
2DUP \ n1 n2 -- n1 n2 n1 n2  
?DUP \ n1 -- n1 [n1]  
NIP \ n1 n2 -- n2  
TUCK \ n1 n2 -- n2 n1 n2  
PICK \ nn..n0 n -- nn..n0 nn  
ROT \ n1 n2 n3 -- n2 n3 n1  
-ROT \ n1 n2 n3 -- n3 n1 n2  
C@ \ addr -- b  
@ \ addr -- n  
2@ \ addr -- d  
C! \ b addr --  
! \ n addr --  
2! \ d addr --  
+! \ n addr --  
NOOP \ -- ; dummy  
WITHIN \ n1|u1 n2|u2 n3|u3 -- flag  
ON \ addr --  
OFF \ addr --  
BOUNDS \ addr len -- addr+len addr  
NAME> \ nfa -- cfa  
>NAME \ cfa -- nfa  
SEARCH-WORDLIST \ c-addr u wid --0|xt  
1|xt -1

## 4.11 Portability words

CELL \ -- 2  
ALIGNED \ addr --addr0  
>BODY \ xt -- pfa  
COMPILE, \ addr --

## 4.12 Defining words

DOES> \ C: colon-sys1 -- colon-sys2 ;  
Run: -- ; R nest-sys --  
\ C: "<spaces>name" -- colon-sys ;  
Exec i\*x --j\*x ; R -- nest-sys  
CONSTANT \ x "<spaces>name" -- ; Exec  
--x  
EQU \ x "<spaces>name" -- ; Exec --x  
VARIABLE \ "<spaces>name" -- ; Exec  
--a-addr  
USER \ u "<spaces>name" -- ; Exec  
--addr  
DEFER \ Comp "<spaces>name" -- ; Run:  
i\*x --j\*x

## MSP430 Lite Target Glossary - User WORDS of stand alone version - MCU is MSP430G2553

' <action> IS <deferredword> ['] <action> IS <deferredword>	RHERE \ -- addr RALLOC \ n -- ROM \ -- RAM \ -- ALIGNED \ addr --addr' ALIGN \ -- , \ x -- C, \ char --				
<b>4.13 Multitasker hook</b> PAUSE \ --					
<b>4.14 Reboot</b> REBOOT \ --					
<b>5.1 User variables</b> SELF \ addr S0 \ -- addr R0 \ -- addr 'TIB \ -- addr #TIB \ -- n >IN \ -- n OUT \ -- n DPL \ -- addr OPVEC \ -- addr IPVEC \ -- addr PAD \ -- addr	<b>5.5 Dictionary management</b> FIND \ c-addr -- c-addr 0 xt 1 xt -1 .NAME \ nfa -- CREATE \ -- <BUILDS \ --	<b>5.9 String input and output</b> ." \ "ccc<quote>" -- \$. \ c-addr -- ACCEPT \ c-addr +n1 --+n2 TYPE \ c-addr len --			S" \ Comp "ccc<quote>" -- ; Run: --c- addr u C" \ Comp "ccc<quote>" -- ; Run: --c- addr LITERAL \ Comp x -- ; Run: -- x [CHAR] \ Comp "<spaces>name" -- ; Run: -- char [ \ -- ] \ -- IMMEDIATE \ -- ' \ "<spaces>name" --xt ['] \ Comp "<spaces>name" -- ; Run: --xt [COMPILE] \ "<spaces>name" -- ( \ "ccc<paren>" -- \ \ "ccc<eol>" -- , \ "ccc<quote>" -- IS \ "<spaces>name" -- EXIT \ R nest-sys -- ; \ C: colon-sys -- ; Run: -- ; R nest-sys -- INTERPRET \ -- EVALUATE \ i*x c-addr u -- j*x QUIT \ -- ; R i*x --
<b>5.2 System data</b> <b>5.2.1 Constants</b> BL \ -- char	<b>5.6 String compilation</b> <b>5.7 ANS words CATCH and THROW</b> (see user manual) <b>5.7.3 User words</b> CATCH \ i*x xt -- j*x 0 i*x n THROW \ k*x n -- k*x i*x n ?THROW \ flag throw- -- ; ABORT" \ Comp "ccc<quote>" -- ; Run: i*x x1 --  i*x ; R j*x --  j*x	<b>5.10 Source input control</b> QUERY \ --	<b>5.11 Text scanning</b> PARSE \ char "ccc<char>" --c-addr u WORD \ char "<chars>ccc<char>" --c- addr	<b>5.12 Miscellaneous</b> WORDS \ -- MOVE \ addr1 addr2 u -- DEPTH \ ??? -- +n TESTAPP \ -- ; endless loop, reset to leave loop.	
<b>5.2.2 System variables and data</b> FENCE \ -- addr DP \ -- addr RP \ -- addr XDP \ --addr DISK-ERROR \ -- addr	<b>5.8 Formatted and unformatted i/o</b> 5.8.1 Setting number bases HEX \ -- DECIMAL \ --	<b>5.13 Wordlist control</b> (see user manual)	<b>5.14 Control structures</b> DO \ C: -- do-sys ; Run: n1 u1 n2 u2 -- ; R -- loop-sys ?DO \ C: -- do-sys ; Run: n1 u1 n2 u2 -- ; R --   loop-sys LOOP \ C: do-sys -- ; Run: -- ; R loop-sys1 --   loop-sys2 +LOOP \ C: do-sys -- ; Run: n -- ; R loop-sys1 --   loop-sys2 BEGIN \ C: -- dest ; Run: -- AGAIN \ C: dest -- ; Run: -- UNTIL \ C: dest - ; Run: x -- WHILE \ C: dest -- orig dest ; Run: x -- REPEAT \ C: orig dest -- ; Run: -- IF \ C: -- orig ; Run: x-- THEN \ C: orig -- ; Run: -- ELSE \ C: orig1 -- orig2 ; Run: -- RECURSE \ Comp --	<b>5.16 Startup</b> <b>5.16.1 The COLD sequence</b> COMMIT \ xt 0 -- EMPTY \ -- COLD \ --	<b>6. Time Delays</b> TICKS \ -- n LATER \ n --n' TIMEDOUT? \ n -- flag MS \ n --
<b>5.3 Vectored I/O handling</b> (see user manual) <b>5.3.3 Generic I/O words</b> KEY \ --char KEY? \ --flag EMIT \ --char CR \ -- SPACE \ -- SPACES \ n --	<b>5.8.2 Numeric output</b> HOLD \ char -- # \ ud1 -- ud2 #S \ ud1 -- ud2 <# \ -- #> \ xd -- c-addr u D.R \ dn -- D. \ d -- . \ n -- U. \ u -- .R \ n1 n2 --			<b>7. Debug tools</b> DUMP \ addr len -- .S \ i*x -- i*x	
<b>5.4 Laying data in memory</b> HERE \ -- addr ORG \ addr -- ALLOT \ n --	<b>5.8.3 Numeric input</b> +DIGIT \ d1 n --d2 >NUMBER \ ud1 c-addr1 u1 --ud2 c- addr2 u2 NUMBER? \ \$addr -- n1 d2 0	<b>5.15 Target interpreter and compiler</b> POSTPONE \ Comp "<spaces>name" --		<b>8. Compile source from AIDE</b> END-LOAD \ -- INCLUDE \ "<filename>" --	<b>11.2 UART</b> CONSOLE0 \ -- addr CONSOLE \ -- addr

TYPE \ c-addr len --

## 12. Ticker using watchdog timer

<TICKS> \ -- addr ; variable

TICKS \ -- n

LEDACTIVE \ -- addr

START-CLOCK \ --

STOP-CLOCK \ --

## 13.1 Basic port usage

(see user manual)

GREEN-ON \ -- ; P1.6

GREEN-OFF \ -- ; P1.6

RED-ON \ -- ; P1.0

RED-OFF \ -- ; P1.0

## 13.2 Port counting using interrupts

## 13.3 Simple ADC driver

(see user manual)

## 5.17 Kernel error codes

-1 ABORT

-2 ABORT"

-4 Stack underflow

-13 Undefined word.

-14 Attempt to interpret a compile only definition.

-22 Control structure mismatch

-unbalanced control structure.

-121 Attempt to remove with MARKER or FORGET below FENCE in protected dictionary.

-403 Attempt to compile an interpret only definition.

-501 Error if not LOADING block.

## 3. MSP430G2553 start up

### 3.1 Magic addresses

\$10FF CALBC1\_1MHz

\$10FE CALDCO\_1MHz

\$10FD CALBC1\_8MHz

\$10FC CALDCO\_8MHz

\$10FB CALBC1\_12MHz

\$10FA CALDCO\_12MHz

\$10F9 CALBC1\_16MHz

\$10F8 CALDCO\_16MHz

\$10F7 08 Size (bytes) of value data

\$10F6 01 Tag

### Port 1 is used as follows

P1.0 Green LED, high=on

P1.1 UART Rx

P1.2 UART Tx

P1.3 Button Switch input

P1.4 --

P1.5 --

P1.6 Red LED, high=on

P1.7 --

### 3.2 Start of Forth

### 3.3 Default Interrupt vectors

### 3.4 Reset values for user and system variables

(see user manual)

## 4. MSP430 definitions

### 4.1 Register usage

IP R0/PC

RSP R1/SP

R2/CG1/SR

R3/CG2

PSP R4

TOS R5

UP R6

LP (locals) R7

scratch R8..R13

codegen R14 temp

codegen R15 SR

## Memory Map

.. peripherals

\$0200 - \$0400 RAM

\$1000 - \$10FF INFO 4 segments of 64 bytes flash

.. nc

\$C000 - \$CFFF application flash (4K)

\$D000 - \$FFFF liteLP2553sa code

(12287 bytes)

### RAM downwards

\$0400 ( R0 @ )

... returnstack, &64 (\$40) bytes down

\$03BC ( S0 @ )

... datastack, &64 (\$40) bytes down

\$032C ( PAD )

... scratch pad

\$02E0

... terminal input buffer, &64

(\$40) bytes up

\$02A0 ( 'TIB @ )

... unused RAM, &40 cells up,

variables

\$024E ( RHERE )

... system user variables

\$0200

INFO segments

\$1000 info-D

\$1040 info-C APPSTART forth system information.

\$1080 info-B

\$10C0 info-A Segment A contains calibration data.

(M.Kalus, 10/2014)

# MSP430 Lite Target Glossary - User WORDS of stand alone version - MCU is MSP430G2553

Alphanumeric List	loop-sys1 --   loop-sys2	C! \ b addr --	EQU \ x "<spaces>name" -- ; Exec --x
: \ C: "<spaces>name" -- colon-sys ;	< \ n1 n2 -- t/f	C!F \ b addr --	ERASE \ addr len --
Exec i*x --j*x ; R -- nest-sys	<# \ --	C" \ Comp "ccc<quote>" -- ; Run: --	EVALUATE \ i*x c-addr u -- j*x
- \ n1 n2 -- n1-n2	<= \ n1 n2 -- t/f	c-addr	EXECUTE \ xt --
-ROT \ n1 n2 n3 -- n3 n1 n2	<> \ n1 n2 -- flag	C@ \ addr -- b	EXIT \ R nest-sys --
, \ x --	<BUILDS \ --	CATCH \ i*x xt -- j*x 0 i*x n	FENCE \ -- addr
; \ C: colon-sys -- ; Run: -- ; R	<TICKS> \ -- adr	CELL \ -- 2	FILL \ addr len char --
nest-sys --	= \ n1 n2 -- flag	CMOVE \ source dest len --	FIND \ c-addr -- c-addr 0 xt 1 xt -1
! \ n addr --	> \ n1 n2 -- t/f	CMOVE> \ source dest len --	FLERASE \ addr len --
!F \ w addr --	>= \ n1 n2 -- t/f	COLD \ --	GREEN-OFF \ -- ; Pl.6
?DO \ C: -- do-sys ; Run: n1 u1 n2 u2	>BODY \ xt -- pfa	COMMIT \ xt 0 --	GREEN-ON \ -- ; Pl.6
-- ; R --   loop-sys	>IN \ -- n	COMPILE, \ addr --	HERE \ --addr
?DUP \ n1 -- n1 [n1]	>NAME \ cfa -- nfa	CONSOLE \ -- addr	HEX \ --
?THROW \ flag throw- -- ;	>NUMBER \ ud1 c-addr1 u1 --ud2 c-	CONSOLE0 \ -- addr	HOLD \ char --
. \ n --	addr2 u2	CONSTANT \ x "<spaces>name" -- ; Exec I \ --n	IF \ C: -- orig ; Run: x--
." \ "ccc<quote>" --	>R \ x -- ; R -- x	--x	IMMEDIATE \ --
.NAME \ nfa --	\$. \ c-addr --	COUNT \ addr --addr+1 len	INCLUDE \ "<filename>" --
.R \ n1 n2 --	0< \ n -- flag	CR \ --	INTERPRET \ --
.S \ i*x -- i*x	0<> \ n -- flag	CREATE \ --	INVERT \ n1 --n2
' <action> IS <deferredword>	0= \ n -- flag	D- \ d1 d2 -- d1-d2	IPVEC \ -- addr
' \ "<spaces>name" --xt	0> \ n -- flag	D. \ d --	IS \ "<spaces>name" --
'TIB \ -- addr ; user variable	1- \ n -- n-1	D.R \ dn --	J \ --n
", \ "ccc<quote>" --	1+ \ n -- n+1	D+ \ d1 d2 -- d3	KEY \ --char
( \ "ccc<paren>" --	2- \ n -- n-2	D< \ d1 d2 -- t/f	KEY? \ --flag
(") \ --addr	2! \ d addr --	D= \ d1 d2 -- t/f	LATER \ n --n'
[ \ --	2@ \ addr -- d	D> \ d1 d2-- t/f	LEAVE \ --
['] <action> IS <deferredword>	2* \ n1 -- n2	D0= \ d -- t/f	LEDACTIVE \ -- addr
['] \ Comp "<spaces>name" -- ; Run:	2/ \ n1 -- n2	DABS \ d1 -- d1	LITERAL \ Comp x -- ; Run: -- x
-- xt	2+ \ n -- n+2	DECIMAL \ --	LOOP \ C: do-sys -- ; Run: -- ; R
[CHAR] \ Comp "<spaces>name" -- ;	2DROP \ n1 n2 --	DEFER \ Comp "<spaces>name" -- ; Run:	loop-sys1 --   loop-sys2
Run: -- char	2DUP \ n1 n2 -- n1 n2 n1 n2	i*x --j*x	LSHIFT \ x count --x0
[COMPILE] \ "<spaces>name" --	2OVER \ n1 n2 n3 n4 -- n1 n2 n3 n4 n1	DEPTH \ ??? -- +n	M* \ n1 n2 -- d
] \ --	n2	DIGIT \ char base -- 0   n true	MAX \ n1 n2 -- max(n1,n2)
@ \ addr -- n	2SWAP \ n1 n2 n3 n4 -- n3 n4 n1 n2	DISK-ERROR \ -- addr ; variable	MIN \ n1 n2 -- min(n1,n2)
* \ n1 n2 -- n1*n2	ABORT" \ Comp "ccc<quote>" -- ; Run:	DNEGATE \ d1 ---d1	MOD \ n1 n2 -- rem
/ \ n1 n2 -- quot	i*x x1 --  i*x ; R j*x --  j*x	DO \ C: -- do-sys ; Run: n1 u1 n2 u2	MOVE \ addr1 addr2 u --
/MOD \ n1 n2 --rem quot	ABS \ n1 -- n1	-- ; R -- loop-sys	MS \ n --
/SRTING \ addr len n -- addr+n len-n	ACCEPT \ c-addr +n1 --+n2	DOES> \ C: colon-sys1 -- colon-sys2 ;	MU/MOD \ ud1 u2 --u3 ud4
\ \ "ccc<eol>" --	AGAIN \ C: dest -- ; Run: --	Run: -- ; R nest-sys --	NAME> \ nfa -- cfa
# \ ud1 -- ud2	ALIGN \ --	DP \ -- addr	NEGATE \ n1 ---n1
#> \ xd -- c-addr u	ALIGNED \ addr --addr0	DPL \ -- addr	NIP \ n1 n2 -- n2
#S \ ud1 -- ud2	ALIGNED \ addr --addr'	DROP \ n1 --	NOOP \ -- ; dummy
#TIB \ -- n	ALLOT \ n --	DUMP \ addr len --	NUMBER? \ \$addr -- n1 d2 0
+ \ n1 n2 -- n1+n2	AND \ n1 n2 -- n3	DUP \ n1 -- n1 n1	OFF \ addr --
+! \ n addr --	BEGIN \ C: -- dest ; Run: --	ELSE \ C: orig1 -- orig2 ; Run: --	ON \ addr --
+DIGIT \ d1 n --d2	BL \ -- char	EMIT \ --char	OPVEC \ -- addr
+LOOP \ C: do-sys -- ; Run: n -- ; R	BOUNDS \ addr len -- addr+len addr	EMPTY \ --	OR \ n1 n2 -- n3
	C, \ char --	END-LOAD \ --	

**MSP430 Lite Target Glossary - User WORDS of stand alone version - MCU is MSP430G2553**

ORG \ addr --	TIMEDOUT? \ n -- flag
OUT \ -- n	TUCK \ n1 n2 -- n2 n1 n2
OVER \ n1 n2 -- n1 n2 n1	TYPE \ c-addr len --
PAD \ -- addr	TYPE \ c-addr len --
PARSE \ char "ccc<char>" --c-addr u	U. \ u --
PAUSE \ --	U< \ n1 n2 -- flag
PICK \ nn..n0 n -- nn..n0 nn	U> \ n1 n2 -- flag
PLACE \ c-addr1 u c-addr2 --	U2/ \ n1 -- n2
POSTPONE \ Comp "<spaces>name" --	UM* \ u1 u2 -- ud
QUERY \ --	UM/MOD \ u32 u16 --urem uquot
QUIT \ -- ; R i*x --	UNLOOP \ --
R@ \ --x ; R x -- x	UNTIL \ C: dest - ; Run: x --
R> \ --x ; R:x--	UPC \ char -- char0
R0 \ -- addr	UPPER \ c-addr len --
RALLOT \ n --	USER \ u "<spaces>name" -- ; Exec
RAM \ --	--addr
REBOOT \ --	VARIABLE \ "<spaces>name" -- ; Exec
RECURSE \ Comp --	--a-addr
RED-OFF \ -- ; P1.0	WHILE \ C: dest -- orig dest ; Run: x
RED-ON \ -- ; P1.0	--
REPEAT \ C: orig dest -- ; Run: --	WITHIN \ n1 u1 n2 u2 n3 u3 -- flag
RHERE \ --addr	WORD \ char "<chars>ccc<char>" -- c-
ROM \ --	addr
ROT \ n1 n2 n3 -- n2 n3 n1	WORDS \ --
RP \ -- addr	XDP \ --addr
RSHIFT \ x count --x0	XOR \ n1 n2 -- n3
S" \ Comp "ccc<quote>" -- ; Run: --	
c-addr u	
S= \ addr1 addr2 count --flag	
S>D \ n -- d	
S0 \ -- addr	
SCAN \ caddr u char -- caddr2 u2	
SEARCH-WORDLIST \ c-addr u wid --0 xt	
1 xt -1	
SELF \ task identifier and TCB	
SKIP \ c-addr u char -- 'c-addr 'u	
SM/REM \ d n --rem quot	
SPACE \ --	
SPACES \ n --	
START-CLOCK \ --	
STOP-CLOCK \ --	
SWAP \ n1 n2 - n2 n1	
TESTAPP \ -- ; enless loop, reset to	
leave loop.	
THEN \ C: orig -- ; Run: --	
THROW \ k*x n -- k*x i*x n	
TICKS \ -- n	
TICKS \ -- n	