

# MPE xARM/Cortex

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## First steps – STM3240G-EVAL board



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## Book name

Manual revision 7.10

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## Software

Software version 7.10

|                |                |            |
|----------------|----------------|------------|
| <b>Package</b> | <b>Number:</b> | XARMCTX/71 |
|----------------|----------------|------------|

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# 1

## Setting up

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We assume that you are using a supported board. For the STM32F4 family, one supported board is the STM3240G-EVAL board.

Find the directory containing the MPE control file

STM32F4eval.ctl

and note it down. It should be in

<xArmCortex>/Cortex/Hardware/STM32F4

### AIDE tools

AIDE is a program that provides manages external tools and provides a terminal emulator. Make sure that everything is set up correctly.

- Run AIDE
- In AIDE, use  
IDE -> Cofigure Edit/Locate  
to set up your favourite editor.
- If the STM32F4 is not on AIDE's toolbar, add a new tool using:  
IDE -> External Tools

To use the MPE control file

STM32F4eval.ctl

set the start directory to the containing STM32F4eval.ctl. You will need quotation marks if there are spaces in the path. Some people prefer to uninstall and reinstall to a directory with no spaces in the pathname. The set up in my development system is:

BMP file: c:\buildkit.dev\software\Aide\CM4.bmp

Compiler: c:\buildkit.dev\software\compiler\xArmCortexDev.exe

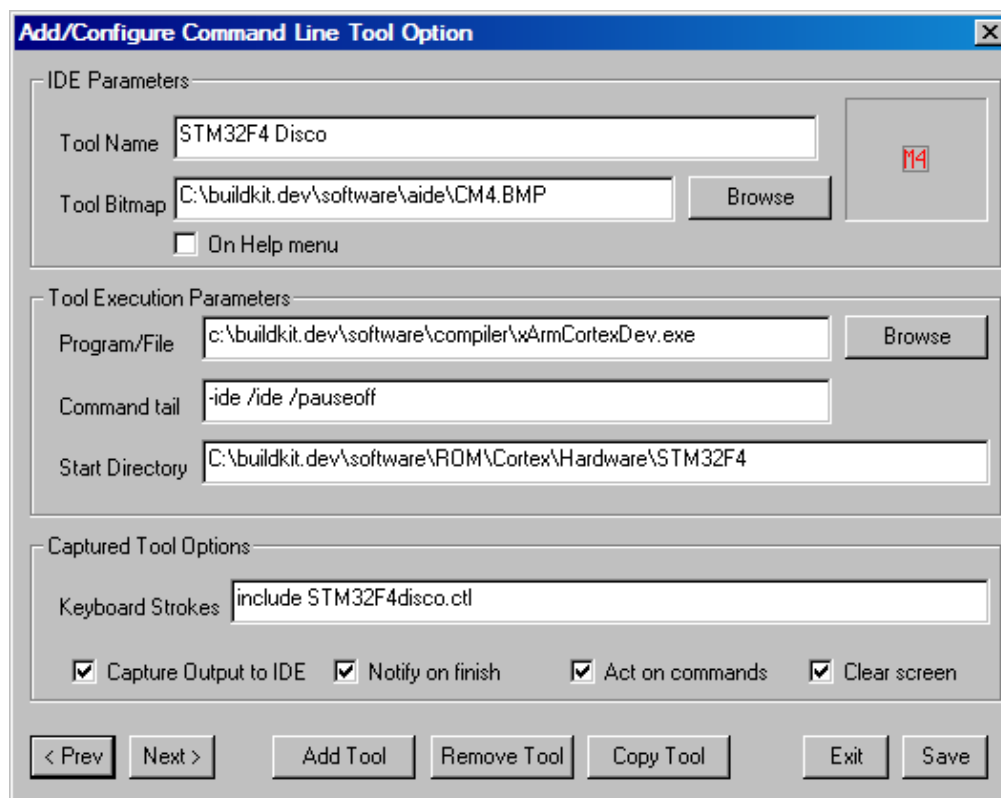
Command tail: -ide /ide /pauseoff

Start dir: C:\buildkit.dev\software\ROM\Cortex\Hardware\STM32F4

Keybd Strokes: include STM32F4eval.ctl

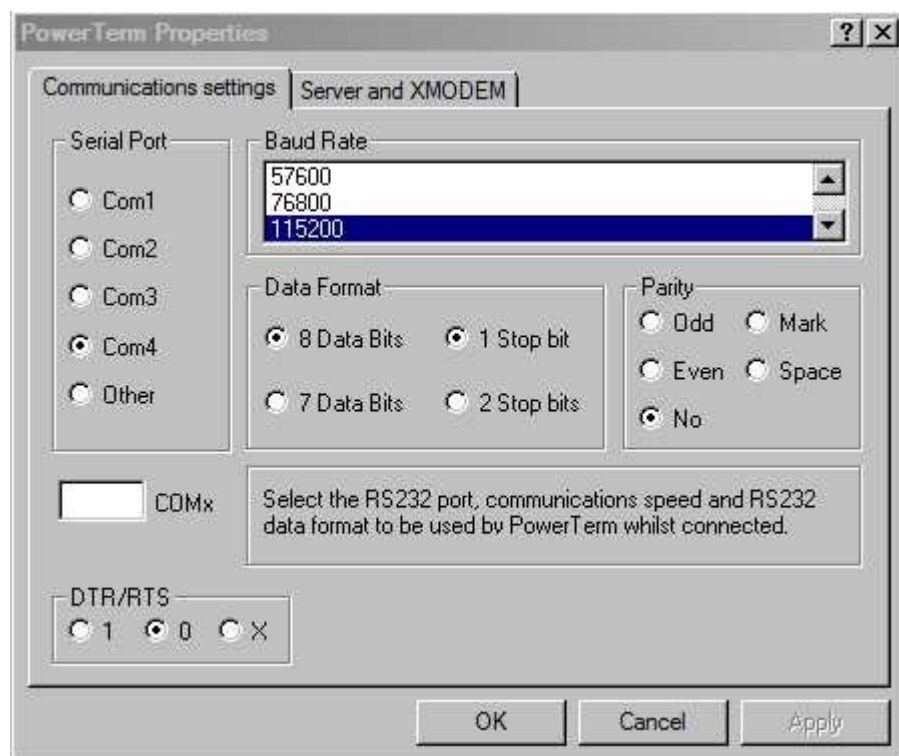
All four checkboxes in the "Captured Tool Options" box are checked. See the following picture for my complete settings. If you are using the Stamp edition of the compiler, change the compiler line:

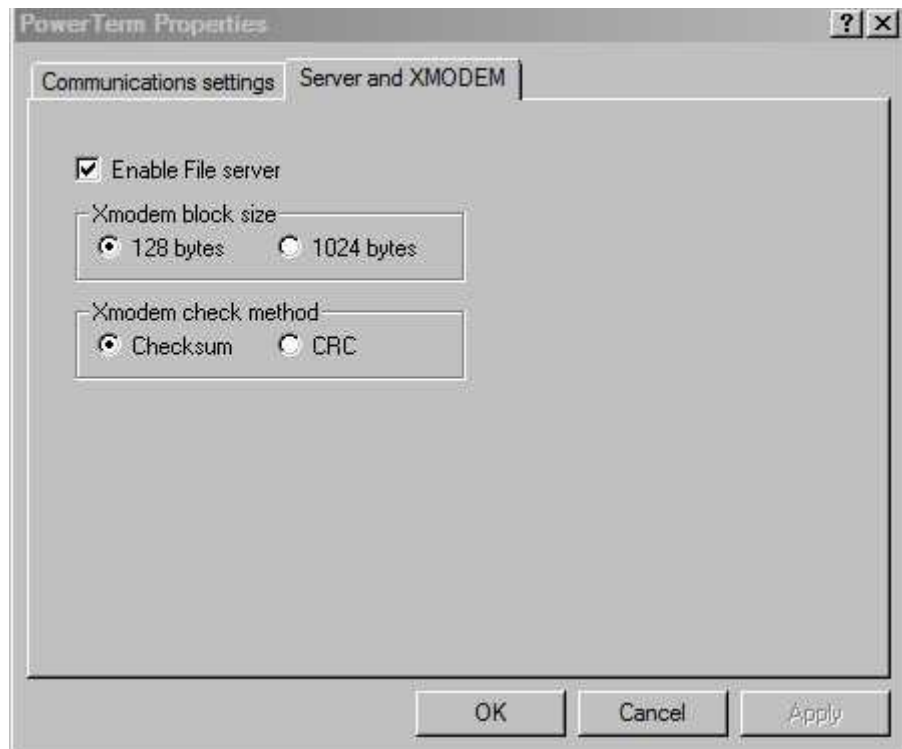
Compiler: c:\buildkit.dev\software\compiler\xArmCortexStamp.exe



## AIDE PowerTerm

Configure PowerTerm for 115200 baud, N81, COMx using the Properties button that is second from the right on the PowerTerm toolbar. Switch to the "Server and XMODEM" page, and check "Enable File Server" with 128 byte and Checksum Xmodem selected.





## Flash Programmer

Download the STM32 ST-LINK Utility from the ST web site. At the time of writing (December 2011), we obtained this from:

<http://www.st.com/internet/evalboard/product/219866.jsp>

The STM3240G-EVAL board includes an integrated ST-LINK. The utility uses it to program the Flash on the chip. If you do not want to use this, you can also use the bootloader built into the STM32F4 devices.

## What you did

You now have the Forth cross compiler tool set up. There will be a corresponding button on the toolbar.

When you click the compiler button the file

STM32F4eval.ctl

is included by the cross-compiler. This file is a control file. It tells the cross compiler how to compile the target and what to compile. Because the tool capture checkboxes were checked when the tool was set up, the compiler runs in the "Tool Capture" window.

## 2 Compiling and testing

### Compile and download

You now have a tool set up. There will be a corresponding button on the toolbar. When you click it, the file

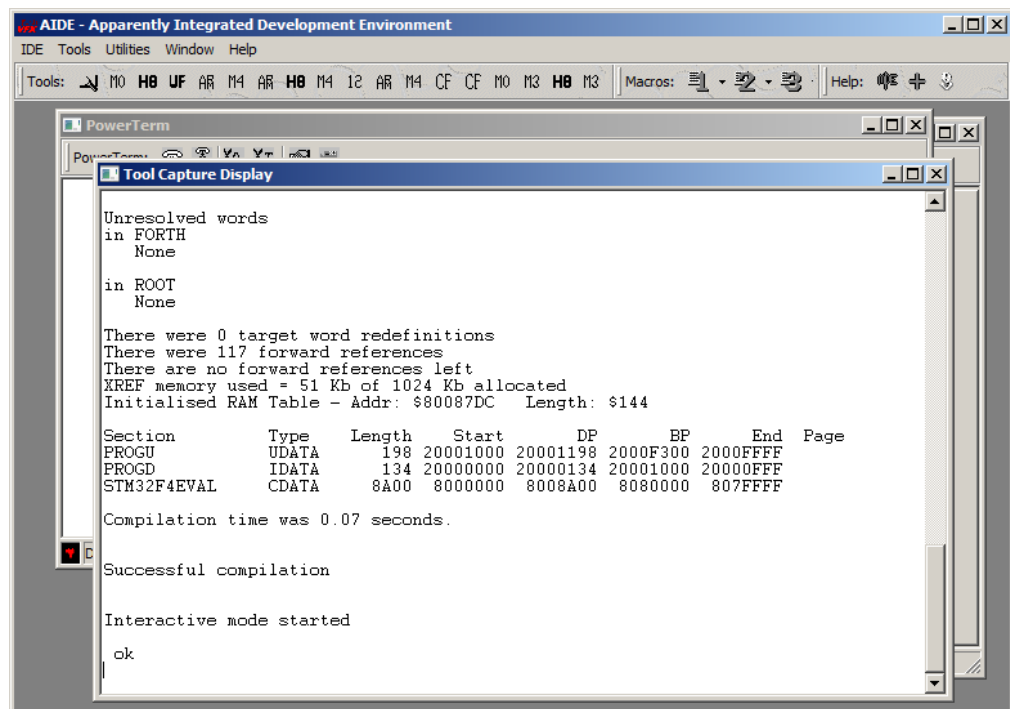
STM32F4eval.ctf

is included by the cross-compiler. This file is a control file. It tells the cross compiler how to compile the target and what to compile. When the compiler has finished, there will be two output files in your STM32F4 folder

STM32F4EVAL.img

STM32F4EVAL.hex

The cross compiler always produces binary image files with a ".img" extension. The control file tells the compiler also to produce an Intel Hex file with a ".hex" extension. The binary files are used by MPE tools, the hex files by some external tools for Flash programming.



AIDE's Tool Capture window should be visible - the compiler is still alive so that you can disassemble words and use the cross reference tools such as XREF and LOCATE. Make sure that PowerTerm is also visible, but is disconnected.

You now need to program one of the output files into the STM32F4 Flash – the STM32 ST-LINK Utility is the most convenient option.

### Configuring the board

Select the required power supply option on JP18.



If you are going to use the RTC and battery, set JP19, 2-3.

If you need to use the I2C EEPROM, fit JP24.

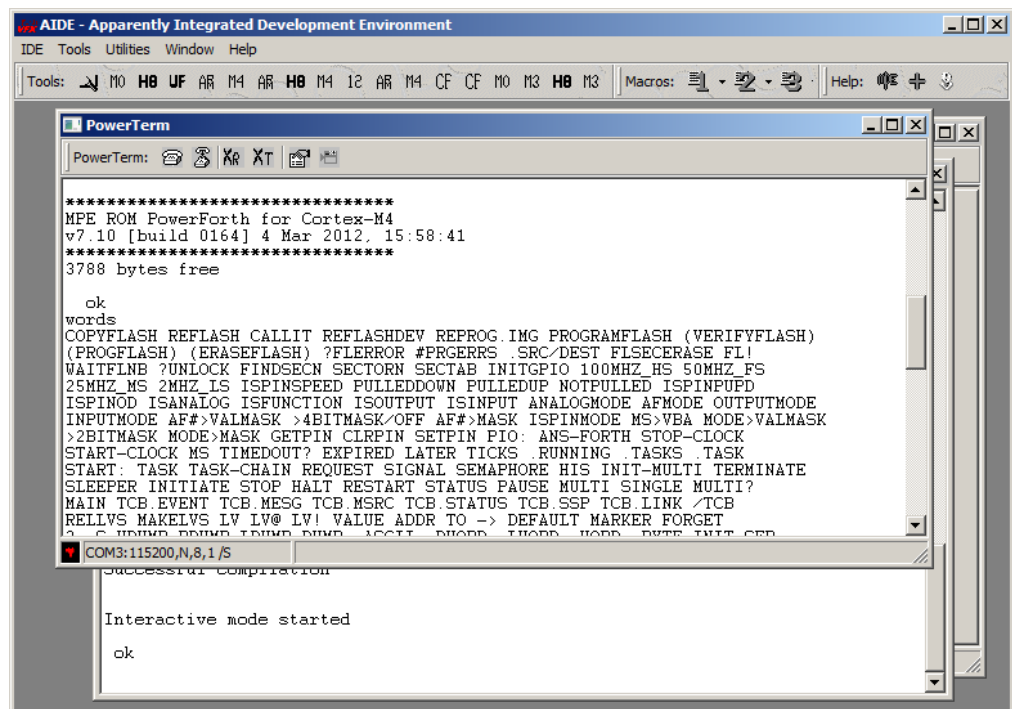
To use USART3 on CN16, set JP22, 1-2.

If you are using Ethernet and PowerNet:

- JP8 – not fitted (MII mode)
- JP6 - 2-3 (MII mode)
- JP5 – 2-3 – in phase 25MHz for PHY from MCO

## Talking to the board

Our software for the STM3240G-EVAL board uses port pins USART3 with port pins PC10 and PC11 on connector CN16. This is the default configuration of the Forth software.



Once the board is programmed and powered, press the PowerTerm connect button (the telephone) and then reset the board. You should now get the MPE PowerForth sign on message. This is the target Forth on the board. You can use this as a normal Forth, and you can even compile code on it. However, since the cross compiler takes a fraction of a second, and the Forth has facilities to reflash itself, it is quicker just to recompile and reflash. To use the Forth facilities, just type

```
REFLASH
```

AIDE will put up a box asking for an image file. Navigate to the STM32F4 folder and select STM32F4EVAL.img. AIDE will download this to the target and reflash the target. If the process has no errors, at the end you will be offered R to reboot. The new target image will then run.

You just repeat the recompile and reflash cycle as required.

If the **REFLASH** operation fails, you will have to use the ST-LINK Utility tool to put the image or hex file into the target again.

## Adding application code

Control files and text macros are important in MPE Forth systems. They are documented in the main cross compiler manual. Please read these sections of the manual.

Although you can just add new lines to the MPE control files, we do not recommend this. You run the risk of losing your changes when you update the compiler. Make your own project. You do this by making a copy of STM32F4eval.ctl and using it as the basis of your project. MPE suggests that you leave our code alone and make a new project that is **not** in the cross compiler folder.

To do this, make a new folder and put the renamed control file in it, (say) MySTM32F4eval.ctl. Also copy over STM32F4eval.no and rename it as (say) mySTM32F4eval .no. Use AIDE's "External Tools" "Copy Tool" button to make a new project button and edit the start directory and include file name.

Edit the control file's text macros that start at line 50 so that they point to the right folders. Edit the build file name on line 332. Now use your new button to recompile and then reflash the new image.

## PowerNet

To use the notes with PowerNet, replace references to the STM32F4eval control and output with STM32F4evalPN.