
A Direct Threaded Code TTL Control Unit

Jack Calderon
Bowling Green, OH

ABSTRACT

A hardware design for a control unit is presented which utilizes TTL logic and fast CMOS Static RAM to implement a very fast version of DTC. The control unit is capable of sequencing the operations required for NEXT in one memory cycle and for DOCOL and SEMI in two memory cycles. A prototype of the control unit is currently being tested for performance evaluation.

