THE NINTH ROCHESTER FORTH CONFERENCE ON INDUSTRIAL APPLICATIONS

University of Rochester

June 13 – 17, 1989

CALL FOR PAPERS

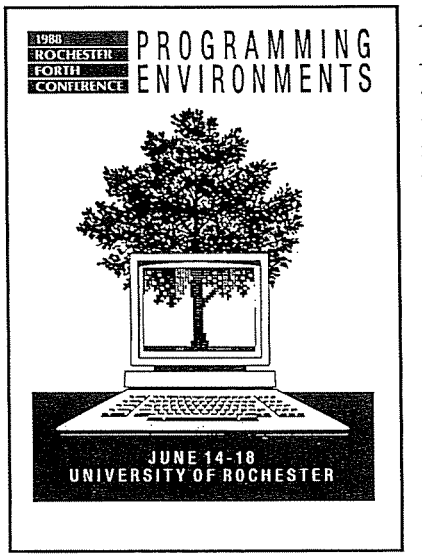
There is a call for papers on the following topics:

Forth-based sensor systems	industrial security systems
process control	materials handling
robotics	tele-operated machinery

In addition, papers on all aspects of Forth application, implementation and technology are being solicited. These include biomedical, avionic and space-based, business and other applications; conventional processor implementations and Forth machines; and the continuing development of Forth tools and the ANS X3J14 Forth Standard.

Papers may be presented in either platform or poster sessions. Please submit a 200 word abstract by May 15th. Papers should be received by June 1st, and are limited to a maximum of four single spaced, camera-ready pages. Longer papers may be presented at the Conference but should be submitted to the refereed Journal of Forth Application and Research. Abstracts and papers should be sent to the Conference Chairman: Lawrence P. Forsley, Institute for Applied Forth Research, Inc. 70 Elmwood Avenue, Rochester, New York 14611, or, electronically to L.Forsley on BIX or LFORSLEY on GENie.

For more information please contact the Conference Chairman or call (716)-328-6426.



Proceedings of the 1988 Rochester Forth Conference . . . . . $25.

7 invited papers and 51 presented papers on all aspects of Forth technology, implementation and its application including these invited papers:

Forth on Unix Workstations
Mitch Bradley, *Sun Microsystems, Inc.*

Cellular Automata Machines: A New Environment for Modeling
Norman Margolus, *MIT Laboratory for Computer Science*

X-Script
Paul Snow and Cliff Click, *KBSI*

ASYST: A Structured Interactive Environment for Scientists and Engineers
Sue Semancik and David Smith, *Asyst Software Technology, Inc.*

RPL: A Mathematical Control Language
W.C. Wickes, *Hewlett Packard*

Other papers include 3 on the New Micro's 68HC11, 5 on the Novix NC4000, 7 on the Harris Forth processor as well as the WISC and the John Hopkin's (Silicon Composer's SCFox) FRISC.

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