# A Debugging Environment for Forth

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#### Abstract

This paper outlines the design and implementation of a visually oriented debugger and execution tracer for Forth. The utility is coded in Turbo Pascal and provides a visual user interface for controlling the execution of a Forth system and a machine-language monitor which performs the context switching between the user interface and Forth and handles a variety of software interrupts.

#### Introduction

A facility to trace the execution of Forth words is a very useful feature, not only for the examination of error conditions during program development, but also as a help for the understanding of forth implementation details.

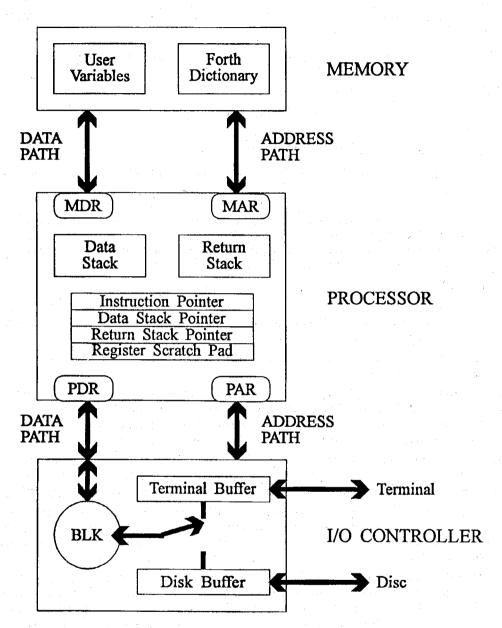
Several approaches to the provision of execution traces have appeared in the Forth literature. These may be classified as follows:

- (1) Simulation of the high-level Forth code definition to determine the run-time behavior of a word. This approach is described in [ASP80], [BLA83], and [BRO83]. The proposal in [BLA83] is noteworthy in that it combines word-by-word interpretation with the use of a screen editor. This represents a first step towards a visual Forth environment.
- (2) Compilation of calls to a break-point monitor or their definition of : and ; to provide such calls. The former strategy is suggested in [BRO83], [JOO83], and [VAN81], while the latter strategy has been adopted in the highly successful public-domain F83 ([LAX85] and [TIN86]).
- (3) Replacement of an executable address in compiled code by the executable address of a debugging monitor. As this break point can be readily moved at run time from one executable address to the next, single-step operation of the monitor becomes possible. This approach is taken in [RUS81] and [SOL84].

None of the above provides for the debugging of words coded in machine language. Yet, as mentioned in the Introduction, Forth implementors need to be able to trace the operation of their systems at the machine-code level. This is especially true when the new system is being generated by meta-compilation, as it is not possible to proceed in an incremental fashion [TIN86]. The rest of the paper is devoted to the description of an environment for Forth which includes a complete machine-level debugger.

# Specification of TRACER

The TRACER program provides a controlled environment in which a Forth processor may be executed and tested. In addition, TRACER provides an animated tracer/debugger, so that the inner workings of the Forth system can be examined as a Forth word executes; in other words, TRACER is designed as a workbench on which to examine existing Forth kernels or test new ones. The TRACER program has two major components: The user interface and the monitor. The former is a Pascal program which gives the user control over the debugging environment.



MAR: Memory Address Register
MDR: Memory Data Register
PAR: Peripheral Address Register
PDR: Peripheral Data Register

Figure 1.
The Architecture of the Abstract Forth Machine.

The latter, written in assembly language, handles context switching between Forth and the user interface. The monitor manages all exceptions generated by the Forth system.

Although the work described in this paper was done in an MS-DOS environment with a particular implementation of Forth [LAX84], the design of the debugging environment was performed in as implementation-independent a manner as possible, on the basis of the concept of an abstract forth machine. As shown in Figure 1, a Forth system can be described in terms of an abstract machine [SOL82] consisting of a cpu with four registers, two stacks, memory, and input and output streams. In the following, this will be referred to as the Abstract Forth Machine (AFM).

This section describes the requirements of the *monitor*, the program that forms the major portion of the debugging environment. The purpose of the program can be summarized as follows:

- (1) Load an executable Forth processor module from disk and, if necessary, perform relocation; establish the correspondence between the AFM and host-machine CPU registers;
- (2) Determine the addresses of all Forth primitive interpreters, namely, those implementing constant, variable, vocabulary, as well as those created by means of the create ... does> mechanism;
  - (3) Establish the structure of the vocabulary used by the Forth implementation being studied;
  - (4) Make available a high-level Forth de-compiler, as well as a low-level Forth dis-assembler.

# Implementation of TRACER

The TRACER program is an animated tracer/debugger. The internal workings of Forth are displayed as execution proceeds and the user can gain a bird's eye view of the entire system. In other words, the prime goal in the design and implementation of TRACER is the provision of a visual interface to Forth giving the user insight into its innermost mechanisms.

The first action of the TRACER program (see Block 1 of Figure 2) is to load a Forth kernel description which consists of (1) the mapping between the AFM and host-machine cpu registers, (2) the host-machine addresses corresponding to the primitive interpreters listed above, as well as the more important constants such as context, dp, sp0, r@, etc., (3) the sizes of the various fields (code, link, name, and view fields) in the words compiled into the Forth dictionary, (4) the structure of the Forth dictionary, including the number of threads used in the vocabulary implementation, and, (5) the name of the Forth executable file. This information allows TRACER to assume control over the execution of the Forth system which it does by opening the Forth file, allocating a memory area to the executable file, and, finally, loading the Forth system and relocating it, if necessary.

The second action of TRACER (Block 2) is to place a breakpoint trap at the start of the Forth inner interpreter (usually implemented as a few host-machine instructions) and transfer control to the Forth cold-boot routine. This generally initializes the Forth system, displays an initial message, and transfers control to the outer or text interpreter. Before this can happen, however, the breakpoint previously set will allow TRACER to regain control and record the values of important variables such as sp0 and rp0. Figure 3 shows the TRACER screen immediately before the startup banner is displayed.

The last action of TRACER (Block 3) is to display a menu and await input from the user. A picture of the initial menu is shown in Fig. 3; here, the top half of the screen shows: The AFM and host-machine registers, the contents of several cells at the top of both data and return stacks, and the contents of the Forth input buffer. Console output from the Forth system is sent to the bottom half of the screen. The list of menus is displayed at the top of the screen. The available options are discussed next.

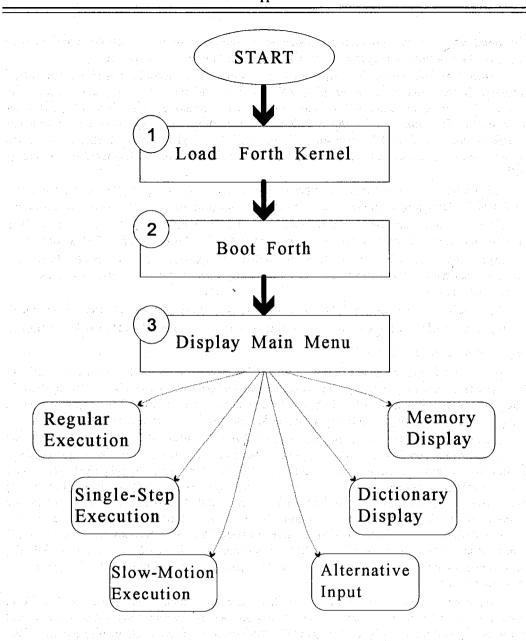


Figure 2.
A General View of the TRACER Options.

# Available Options

The options available from the top-level menus shown in Fig. 3 are described in greater detail in this section in order of their appearance on the console screen.

- 1. System Menu.
- (a) Kernel: Edit Kernel Description. This option allows the user to edit the kernel description mentioned in the preceding section. The invocation of this option results in the pop-up window

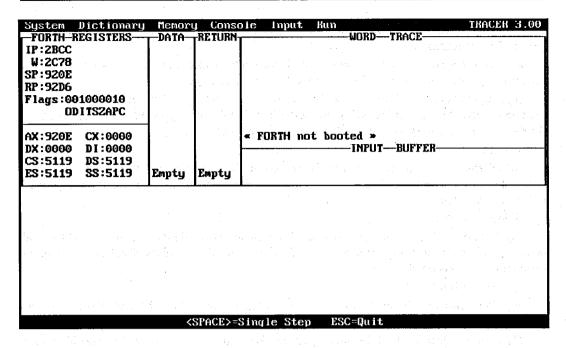


Figure 3.

The Main Display of the TRACER Program.

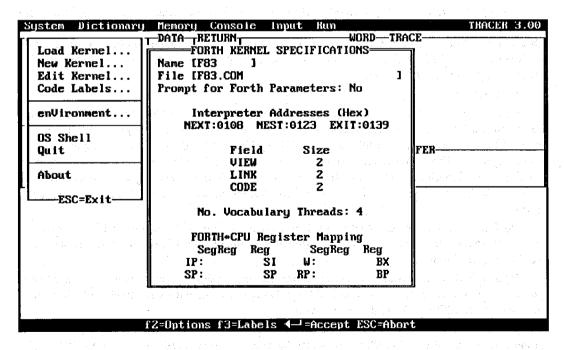


Figure 4.

The Pop-Up Screen for Editing the Abstract Forth Machine Parameters.

shown in Fig. 4. All of the fields shown there can be changed by the user to tailor TRACER to a specific implementation.

- (b) Shell: Invoke Operating System. This is the standard way of temporarily exiting to the operating system to invoke an OS utility, or call a user program.
- (c) Environment: Display Vocabularies and System Parameters. This option is not shown on the menu screen because of lack of space on the bottom line, but a press of the E key results in the display of some frequently consulted Forth-system parameters.

#### 2. Dictionary Menu.

(a) Dictionary: Decompiler/Disassembler Option. This is the decompiler and disassembler option that can be used to obtain a dictionary oriented display of individual Forth words—be they colon or code words.

#### 3. Memory Menu.

(a) Memory: Display Memory Contents. This menu option yields a display in ASCII and hexadecimal of the memory area used by the Forth system. The contents of individual memory locations can be changed.

#### 4. Console Menu.

- (a) Zoom In/Out. This allows the user to switch between the TRACER screen and the conventional Forth output screen.
- (b) Options: Modify AFM Display Updating. Here, the user is allowed to specify which sub-windows of the AFM display shown in Fig. 3 are to be updated during auto-execution of TRACER.

#### 5. Input Menu.

(a) Input: Alternative-Input Option. Here, the Forth input stream is switched from the standard terminal input buffer to a separate buffer whose contents can be edited as needed. The alternative buffer is displayed on the screen and used by TRACER to supply input to Forth. The buffer can be refilled by the user when it empties. Input can be switched back to the regular terminal input buffer at any time.

#### 6. Run Menu.

- (a) Go: Regular Execution from Specified Address. Here, Forth is allowed to execute without any constraints starting from an address entered by the user when requested by a pop up secondary menu which requests a sixteen-bit value for (CS,IP). The TRACER program can regain control, however, when (1) a user-placed breakpoint is encountered, (2) the user presses a user-defined break-key combination which results in a forced transfer of control even if no break points have been set (a feature used to escape from infinite loops), and (3) the Forth system terminates normally.
- (b) Run: Resume Normal Execution. Here, Forth is allowed to execute without any constraints from the current address, i.e., the value of IP at the point at which the TRACER program has last regained control.
- (c) Single-Step Execution. As the name implies, this option is used to step through a Forth program one word at a time. It is invoked by pressing the space bar on the keyboard. Control is returned to TRACER at the end of every word.
- (d) Auto: Slow-Motion Execution. In this mode of operation, there is a user adjustable pause at the completion of every Forth word. As in the case of regular Forth execution, the TRACER program is re-entered whenever the user presses the break-key combination, a user-placed breakpoint is encountered, or the Forth program terminates normally. In addition, the pressing of any key during slow-motion execution also allows TRACER to regain control.

The next section deals with the operations performed by TRACER when it is given control from any of the main-menu options.

# Context Switching Between TRACER and Forth

Figure 5 shows a block diagram of the TRACER module that handles the three separate execution modes specified through the main menu. Each of the blocks in Fig. 5 is discussed below.

- 1. Initialize TRACER. The main purpose of this block is to place breakpoints at the start of the NEXT, NEST, and UNNEST interpreters, so that control can be returned to TRACER at these critical stages of the AFM operation. This block is also used to install any user-specified breakpoints.
- 2. Call Machine-Code Interface. Call external procedure as entry point to machine code interface.
- 3. Save TRACER Registers. The cpu registers relevant to the operation of TRACER are saved. These include the hardware stack register.
- 4. Set I/O Traps. All changes to the interrupt vectors required to install both user breakpoints and the user-defined break-key combination are made at this stage.
- 5. Restore Forth Registers. This block takes care of the placing of the appropriate values into all AFM registers (including the Forth stack pointers). These values are the result of the initialization process shown in Fig. 2 or of a previous invocation of the Save Forth Registers (Block 8 in Fig. 5).
- 6. Display Machine State. Here, the display of the AFM and cpu registers, the data and return stacks, the current input buffer, and the current Forth word is updated on the screen.
- 7. Transfer to Forth. Normal Forth execution is resumed at this stage through the execution of a return-from-interrupt instruction.

Once Forth execution is re-started as indicated in the last operation, it will proceed autonomously until interrupted by a breakpoint event or a trapped Forth I/O request. The former maybe caused by (1) a user-inserted trap, (2) a trap that implements single-step or slow-motion execution, (3) the normal termination of the Forth program, or (4) a hot-key interrupt.

The course of TRACER actions in the case of a breakpoint event is as follows:

- 8. Save Forth Registers. All AFM registers are saved.
- 9. Remove I/O Traps. The changes to the interrupt vectors made before transferring to the Forth system are undone.
- 10. Restore TRACER Registers. The register contents appropriate to TRACER operation (including the hardware-stack register) are restored.
- 11. Slow-Motion-Execution Control. If slow-motion mode is in effect, the cycle shown in Fig. 5 is repeated for the next Forth word after a user-specified delay. Otherwise, control is transferred back to the routine which handles the main menu.

The sequence of actions in the case of a trapped Forth I/O event is the following:

- 12. Service I/O Request. In the case of an input request, the next character is supplied from the TRACER input buffer when TRACER is trapping input, otherwise it is taken from the Forth Terminal Input Buffer. When Forth sends a character to the display screen, the Forth output area is scrolled when becomes necessary.
- 13. User-Initiated Break. When the I/O request involves the break-key combination (hot key depressed), processing continues as if this were a breakpoint event (see Fig. 5).

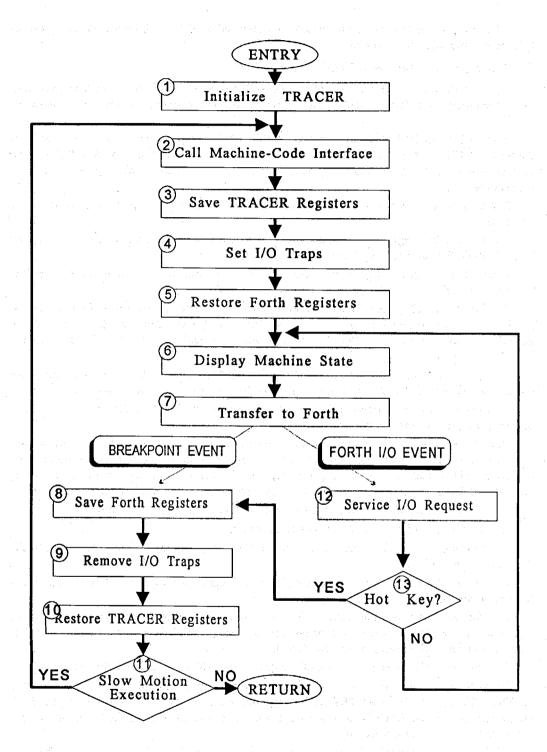


Figure 5.

Details of Context Switching Between TRACER and Forth.

# Details of Operation

Figures 6 to 13 provide a sampling of the capabilities of our debugging system. After the system is booted and is executing the outer interpreter, a simple colon definition is entered by means of the Alternative-Input Option as shown in Fig. 6. It should be noted that trapping of

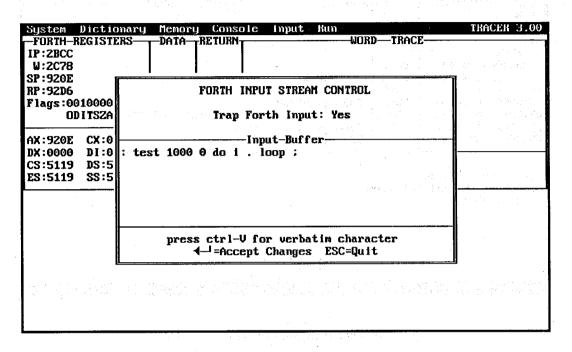


Figure 6.
Use of the Alternative-Input Buffer by Forth.

Forth input is enabled, so that when Forth execution resumes, keyboard input will be taken from the TRACER Input Buffer. The Slow-Motion Option is used to resume Forth execution which results in the alternative input buffer being read by the outer interpreter. Figure 7 shows the compilation process interrupted by the operation of the user-defined break key after the parameters of the loop have been read.

The remaining figures illustrate the exploratory features of TRACER which become available when regular Forth execution is suspended in the middle of a colon definition (see Fig. 7). Figure 8 shows the window displayed when the environment-parameter display is selected. Note that the system is in the compile state (state=0). The manner in which the Forth dictionary can be examined is depicted in Fig. 9. Only one thread of a vocabulary can be displayed at a time. Immediate words are marked by the symbol I in the fourth column. The highlighted word in the display (DUMP in Fig. 9) can be decompiled by pressing the enter key. Cursor keys are used for selection of the word to decompile. The decompiler creates a new window as shown in Fig. 10. The decompiler recognizes different classes of Forth words, including words defined by the create ... does> mechanism and code words (see Fig. 11).

Lastly, Figures 12 and 13 illustrate the use of the Memory-Display Option. The former exhibits the hexadecimal and ASCII memory dump of the lowest area of the F83 system. Figure 13 shows the corresponding disassembly. The cold and warm boot transfers at offsets 0100 and 0103

can be seen, as well as the instructions implementing the Forth inner interpreter (offsets 0106 to 010C, respectively).

System Dictionary		j Cons			TRACER 3.0
-FORTH-REGISTERS-	DATA	RETURN	I	-word-trace-	
IP:086C W:0020 SP:ED00 RP:EDCE		1,,1	and the second second		
Flags:001010000	000D ED0E 0020 0020	10DA	20UER 46B1:10DB 7BRANCH		
DX:EDOE DI:0C41	000D	11AD		INPUT-BUFFER	<del></del>
CS:46B1 DS:46B1	ED0E	11DB	do i . loop :F		
ES:46B1 SS:46B1	0050	2B80			
986 Forth 83 Model ersion 2.1.0 Modif test 1000 0		ı <b>n84</b>			

Figure 7.

A Break-Key Interruption of a Forth Compilation.

System Dictionar		TRACER 3.00
Load Kernel New Kernel Edit Kernel Code Labels	DATA RETURN WORD TRACE	
enVironment  OS Shell Quit	FORTH ENVIRONMENT  VOCABULARIES  Current: FORTH  Context: FORTH	(
About ESC=Exit— 3086 Forth 83 Mod Version 2.1.0 Mod test 1000 0	SYSTEM PARAMETERS State: 0 BLK:0 UP: 2C72 DP:000A TIB: 930E  press any key when ready	

Figure 8.

Display of the Forth Environment during an Interrupted Compilation.

-FORTH-REGI IP:0EAA			VOCAI	BULA	ARY FORTH (5	55 words)		
W:0E88 SP:9206	,_VII	:W—,	rcfa <sub>1</sub>	п		NAME	· ·	1 ( ) 1 ( )
RP:92CE	4	44	60DD		PAGE			1.1
Flags:00100		44	60CZ		LOGO			*
ODITS	4	44	60B5		L/PAGE			-
	4	34	5CCC		(SEE)			1.0
AX:0000 CX	4	30	5BD1		DL			
DX:0000 DI	4	30	_5BB8		DU			
CS:5119 DS	4	30	5B89		DUMP			
ES:5119 SS	4	29	5A95		DLN	•		
	1 1	29	5A4D		D.2	Programme and the second		
Ī	4	21	577A		(WHERE)			1
	4	21	56CC		DONE			1
	4	13	4F56		DARK			
	4	11	4EA5		DELETE			
				Curi	rent Thread	= 1		
		J				Vocabulary		
			<b>†↓</b> A▼:	=Mox	re 4-1≐See	ESC=Quit		l

Figure 9.

Display of the a Colon-Definition Decompilation during an Interrupted Compilation.

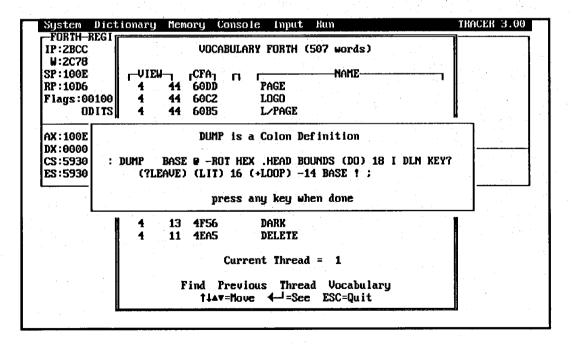


Figure 10.

Display of a Colon-Definition Decompilation during an Interrupted Compilation.

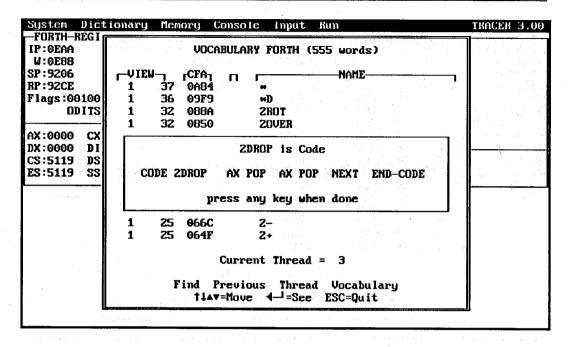


Figure 11.

Display of a Code-Word Disassembly during an Interrupted Compilation.

System Dic -FORTH-REGI										opui				JR <b>D</b> -	—T)	RACE	TRACER 3.00
	00	01	02	03	04	05	06	07	08	09	0A	ов	0C	OD	0E	OF	
5119:0100	E9	34	ZB	E9	2B	ZB	52	50	AD	8B	<b>D8</b>	FF	27	03	10	00	i4+i++RP-·ΧΔ'···
5119:0110																ED	··FORTH·)^fRf=fm
5119:0120	65	00	00	43	43	4D	4D	89	76	00	8B	FЭ	EB	DA	04	10	e··CCMM·v··skZ··
5119:0130	00	00	84	45	58	49	<b>D4</b>	39	01	8B	76	00	45	45	EB	C8	···EXIT9····EEkH
5119:0140	04	10	30	01	86	<b>5</b> 5	4E	4E	45	53	<b>D4</b>	39	01	87	EC	56	··O··UNNEST9··IV
5119:0150	87	EC	5E	43	43	53	EB	BO	43	43	53	EB	AB	05	10	42	·1^CCSk0CCSk+··B
5119:0160	01	82	55	DØ	58	01	72	ZC	43	43	8B	07	EB	99	43	43	··UPX·r,CC··k·CC
5119:0170	8B	07	93	06	66	01	EB	8F	05	10	00	00	85	28	4C	49	· · · · f · k · · · · · · (LI
5119:0180	54	A9	84	01	AD	E9.	7F	FF	<b>09</b>	10	0F	01	86	42	52	41	T)··-ioo····BRA
5119:0190	4E	43	C8	95	91	8B	34	E9	6E	FF	09	10	00	00	87	ЭF	NCH···4ina····?
5119:01A0	<b>4</b> 2	52											E8		46	E9	BRANCH( · X · @thFF i
5119:01B0		FF											A9		01		V4··z··(LOOP)7·8
5119:01C0												46	E9	39	FF	OB	· · · F · qN · E · FF i 94 ·
5119:01D0	10	B4	01	87	28	ZB	4C	4F	4F	<b>50</b>	A9	DD	91	58	EB	EZ	·4··(+LOOP)]·ХкЬ
5119:01E0	OC.															8B	··Q··(DO)k·X[MM·
5119:01F0	14	89	56	00	46	46	81	<b>C3</b>	00	80	4D	4D	89	5E	00	29	··U·FF·C··MM·^·)
		†.	ļ∡∀:	- Mot	)E	ne	ew f	addı	es		Una	1556	emb l	le	ESC	C=Do	ne o
·					-		•			-							
						1			3.50								

Figure 12.

Display of Memory Contents during an Interrupted Compilation.

FORTH-REGIS	0100	E9342B	ASSEMBLE MEMORY————————————————————————————————————	
	0103	E92B2B	11051 JMP	
5119:0100	0106	52	DX PUSH	+RP-·X4' · · ·
5119:0110	0107		AX PUSH	TH · )cfRf=fr
5119:0120	0108	AD	WORD LODS	MM·u·skZ·
5119:0130	0109	8BD8	AX BX MOV	IT9 · · u · EEkl
5119:0140	010B	FF27	[BX] JMP	UNNEST9 - 1
5119:0150	010D	0310	[BX+SI] DX ADD	Sk0CCSk+··I
5119:0160	010F	0000	AL [BX+SI] ADD	·r.CC··k·C
5119:0170	0111	85464F5254	DX [BP+SI+84] TEST	·k · · · · · (L
5119:0180	0116	C8	??? (\$C8)	ioo····BR
5119:0190	0117	9A296366D2	:6329 FAR CALL	4ino
5119:01A0	011C	66	??? (\$66)	H(·X·@thFF:
5119:01B0	011D	3D66ED	-4762 AX CMP	··(LOOP)?-8
5119:01C0	0120	65	777 (\$65)	gN·E·FF19a
5119:01D0	0121	0000	AL [BX+SI] ADD	+L00P)] ·Xkl
5119:01E0	0123	43	BX INC	(DO)k-XEMM
5119:01F0	0124	43	BX INC	F.CMM.^.
	0125	4D	BP DEC	
	0126	4D	BP DEC	
	ــــــا		new Address ESC=Quit-	<u> </u>

Figure 13.

Display of Disassembled Memory Contents during an Interrupted Compilation.

#### Discussion and Conclusions

The TRACER program is an animated tracer/debugger. The internal workings of Forth are displayed as execution proceeds and the user can gain a bird's eye view of the entire system. In other words, the prime goal in the design and implementation of TRACER was the provision of a visual interface to Forth giving the user insight into its innermost mechanisms. The use of TRACER in a graduate course on Forth is planned for the 1991/92 Academic Year. In addition, TRACER has proved to be of considerable value in testing new versions of Forth obtained through metacompilation [Lax85]. At present, TRACER works well with F83 and all functions of the program have been implemented. TRACER has been designed to work equally well with 16-bit or 32-bit addressing models, although the latter feature has not been tested. A desirable feature which has not yet been implemented is a facility for user-inserted breakpoints in Forth words. This will be included in the next version of TRACER.

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